

In the Title:

Amend the title as follows:

~~Tap and linking module for scan access of multiple cores~~
~~with IEEE 1149.1 test access ports IEEE 1149.1 TAP~~
Instruction Scan With Augmented TLM Scan Code

In the Specification:

[0001] This application is related to provisional application serial No. 60/024,884, filed Aug. 30, 1996 and application Ser. No. 08/918,872, filed Aug. 26, 1997, now US 6,073,254, issued June 6, 2000, the contents of each of which are incorporated herein by reference. This application is a divisional of application serial No. 09/981,514, filed October 16, 2001, now US 6,711,707, issued March 23, 2004, which is a divisional of application serial No. 09/277,504, filed March 26, 1999, now US 6,324,662, issued November 27, 2001, which claims the benefit of provisional application serial No. 60/090,913, filed June 26, 1998 and provisional application serial No. 60/079,552, filed March 27, 1998.

[0039] TLM decode 404 receives the TAP state signals 436, control signal 433 from the AISR 410, enable signals 434 from link update register 408, and the reset signal 440. TLM decode 404 outputs an ~~IRSEL~~ IR-SEL signal ~~446~~ 448 to multiplexers ~~416~~ 412 and 414, a capture control signal 444 to AISR 410, ~~TDOSEL~~ TDO-SEL signal 446 to multiplexer 416, update signal 450 to link update register 408, and shift enable signals 452 to link shift register 406.

[0040] AISR 410 receives data output 456 from multiplexer 416, capture, shift, update, and reset bus signals 444 from TLM decode 404, and a fixed 1 and 0 signal input 454. The AISR 410 outputs a data signal 458 to multiplexer 414 and an enable signal ~~432~~ 433 to the TLM decode 404. The AISR 410 responds to bus 444 to perform capture, shift, and update operations during all 1149.1 scan operations.